09/136,680

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Christophe J. Chevallier

Examiner: Nguyen, Luong

Patent No.: 6,879,340

Group Art Unit: 2612

Issue Date: April 12, 2005

Docket No: 703.032US1

Title: CMOS IMAGER WITH INTEGRATED NON-VOLATILE MEMORY

Commissioner for Patents
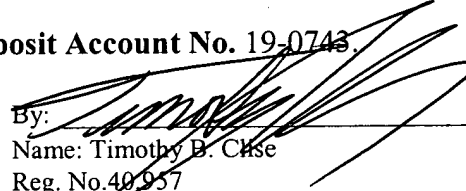P.O. Box 1450
Alexandria, VA 22313-1450

Certificate

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of Correction

We are transmitting herewith the attached:

X   Request for Certificate of Correction.
X   Certificate of Correction Form - PTO-1050 (in duplicate)
X   A return postcard.

Please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

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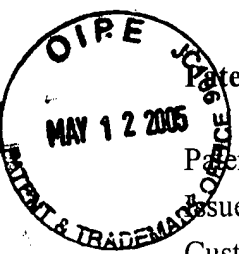Customer No: 21186

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Michaela R. Lewellyn
Name

Michaela Lewellyn
Signature

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IN UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.:	6,879,340	Docket No:	703.032US1
Issue Date:	April 12, 2005	Patentee:	Christophe J. Chevallier
Customer No.:	21186	Confirmation No.:	2027
Title	CMOS IMAGER WITH INTEGRATED NON-VOLATILE MEMORY		

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

As the error is that of the Patent Office, it is believed that no fee is due.

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No.19-0743.

Respectfully Submitted

CHRISTOPHE J. CHEVALLIER

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A
P.O. Box 2938
Minneapolis, MN 55402
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Date :_____ By:_____
                            Timothy B. Clise
                            Reg. No. 40,957
                            TBC:mrl

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Michaela R. Lewellyn   Michaela Lewellyn
Name   Signature

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO   : 6,879,340   Page (1) of 1

DATED   : April 12, 2005

INVENTOR(S)   : Chevallier

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "U.S. Patent Documents", in column 2, line 1, delete "438/69" and insert - - 438/069 - -, therefor.

On the face page, in field (56), under "Other Publications", in column 2, line 7, delete "Features" and insert - - Feature - -, therefor.

In column 1, line 1, in the "Title", delete "CM OS" and insert - - CMOS - -, therefor.

In column 7, line 3, delete "Psilicon" and insert - - P-silicon - -, therefor.

In column 7, line 8, delete "10" and insert - - 110 - -, therefor.

MAILING ADDRESS OF SENDER: PATENT NO. _____6,879,340_____

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Atty Docket No: 703.032US1

MAY 2 3 2005

UNITED STATES PATENT AND TRADEMARK OFFICE
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